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FPGAs in HPC - applications and methods

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In recent years, Field Programmable Gate Array technology has gained momentum in the HPC sector as the abundance of configurable resources and high-level development tools allow to implement complex algorithmics. Unique features of this technology such as adaptable computing, pipelined processing and integrated high-speed transceivers provide means to compete with classic CPUs or GPUs in certain applications. In this talk, I will present the technology fundamentals, development methodologies and overview the computing areas that can benefit from employing FPGAs.

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