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Neural Network inference on FPGA-based platforms

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Field Programmable Gate Arrays (FPGAs) offer unique features for High-Performance Computing such as natural parallelism, streamlined processing, and dynamic reconfiguration creating a relatively new concept of adaptive computing.

Modern device capabilities, high-level development techniques, and market adoption make them a powerful and interesting component for HPC hardware platforms.

In this talk, I will present a technology overview and current techniques for implementing Neural Networks on FPGA-based platforms.

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